

<b>Notice of References Cited</b>	Application/Control No. 10/062,626		Applicant(s)/Patent Under Reexamination YANG, JIANJUN	
	Examiner Jason Proctor		Art Unit 2123	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,349,272	02-2002	Phillips, Joel	703/2
*	B	US-6,188,974	02-2001	Cullum et al.	703/14
*	C	US-5,808,915	09-1998	Troyanovsky, Boris	703/14
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ken Kundert, "Simulation Methods for RF Integrated Circuits", 1997, IEEE, page 752-765
	V	Danial Faria, Lawrence Dunleavy, and Terje Svensen; "The Use of Intermodulation Tables for Mixer Simulations", 2002, Microwave Journal, page 1-6
	W	Iason Vassilou and Alberto Sangiovanni-Vincentelli, "A Frequency-Domain, Volterra Series-Based Behavioral Simulation Tool for RF Systems", 1999, IEEE, pages 21-24
	X	Kartikeya Mayaram, David C. Lee, Shahriar Moinian, David A. Rich, and Jaijeet Roychowdhury; "Computer-Aided Circuit Analysis Tools for RFIC Simulation: Algorithms, Features, and Limitations"; 2000, IEEE, pages 274-286

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.